

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

#1257  
#1 ZMG

<b>TRANSMITTAL OF FORMAL DRAWINGS</b>		Docket Number: 2885/21	Batch No. F54
Application Number 09/335,974	Filing Date June 18, 1999	Examiner RAY, G.	Art Unit 2181
Invention Title I/O AND MEMORY BUS SYSTEM FOR DFPS AND UNITS WITH TWO OR MULTI-DIMENSIONAL PROGRAMMABLE CELL ARCHITECTURES		Inventor(s) VORBACH, et al.	


Assistant Commissioner  
for Patents  
Washington D.C. 20231  
Box Issue Fee

Sir:

Applicants refer to the Notice of Allowability mailed April 25, 2001, in the above-referenced patent application and submit herewith eighteen (18) sheets of formal drawings to be substituted for the drawings which were previously submitted in the application.

The Commissioner is hereby authorized to charge any required fees to Deposit Account No. 11-0600. A duplicate copy of this sheet is enclosed.

Dated: July 25, 2001

By:   
Andrew L. Reibman (Reg. No. 47,893)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231, on

Date July 25, 2001 Atty's Reg. # \_\_\_\_\_

Atty's Signature   
**KENYON & KENYON**

KENYON & KENYON  
One Broadway  
New York, N.Y. 10004  
(212) 425-7200 (telephone)  
(212) 425-5288 (facsimile)